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(AU). MILBURN, Gerard, J. [AU/AU]; 138 Highland Terrace, St Lucia, QLD 4067 (AU). CLARK, Robert, Graham [AU/AU]; 10 Curban Street, Balgowlah Heights, NSW 2093 (AU).

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(74) Agent: F B RICE & CO; 605 Darling Street, Balmain, NSW 2041 (AU).

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(71) Applicant (*for all designated States except US*):
UNISEARCH LIMITED [AU/AU]; Rupert Myers Building, Level 2, Gate 14, Barker Street, UNSW, Sydney, NSW 2052 (AU).

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(72) Inventors; and

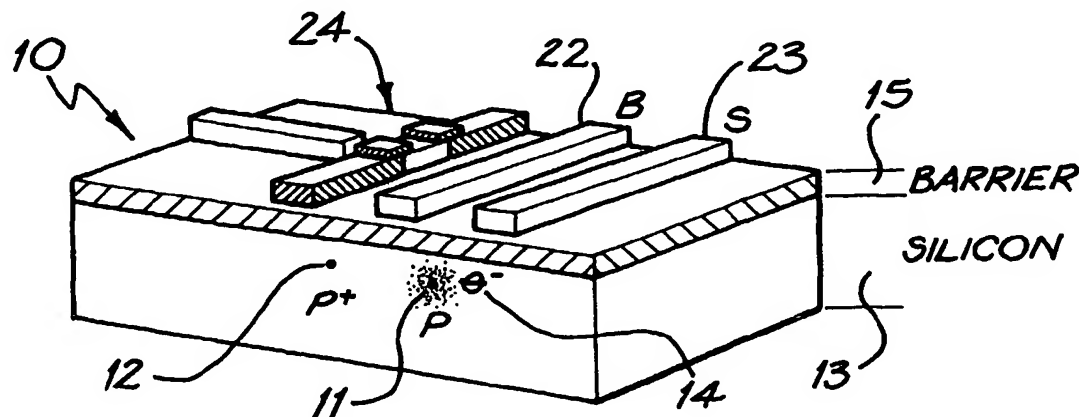
(75) Inventors/Applicants (*for US only*): HOLLENBERG, Lloyd, Christopher, Leonard [AU/AU]; Unit 28, 22 Bosisto Street, Richmond, VIC 3121 (AU). DZURAK, Andrew, Steven [AU/AU]; Unit 22, 6-8 Farrell Avenue, Darlinghurst, NSW 2010 (AU). WELLARD, Cameron [AU/AU]; 7 Groom Street, Clifton Hill, VIC 3068 (AU). HAMILTON, Alexander, Rudolf [GB/AU]; 7/106 Mount Street, Coogee, NSW 2034 (AU). REILLY, David, J. [AU/AU]; 2/61 Illawarra Street, Allawah, NSW 2218

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(54) Title: SOLID STATE CHARGE QUBIT DEVICE



(57) Abstract: Ionisation of one of a pair of dopant atoms (11, 12) in a substrate (13) creates a double well potential, and a charge qubit is realised by the location of one or more electrons or holes (14) within this potential. The dopant atoms may comprise phosphorus atoms, located in a silicon substrate. A solid state quantum computer may be formed using a plurality of pairs of dopant atoms (11, 12), corresponding gate electrodes (22, 23), and read-out devices comprising single electron transistors (24).